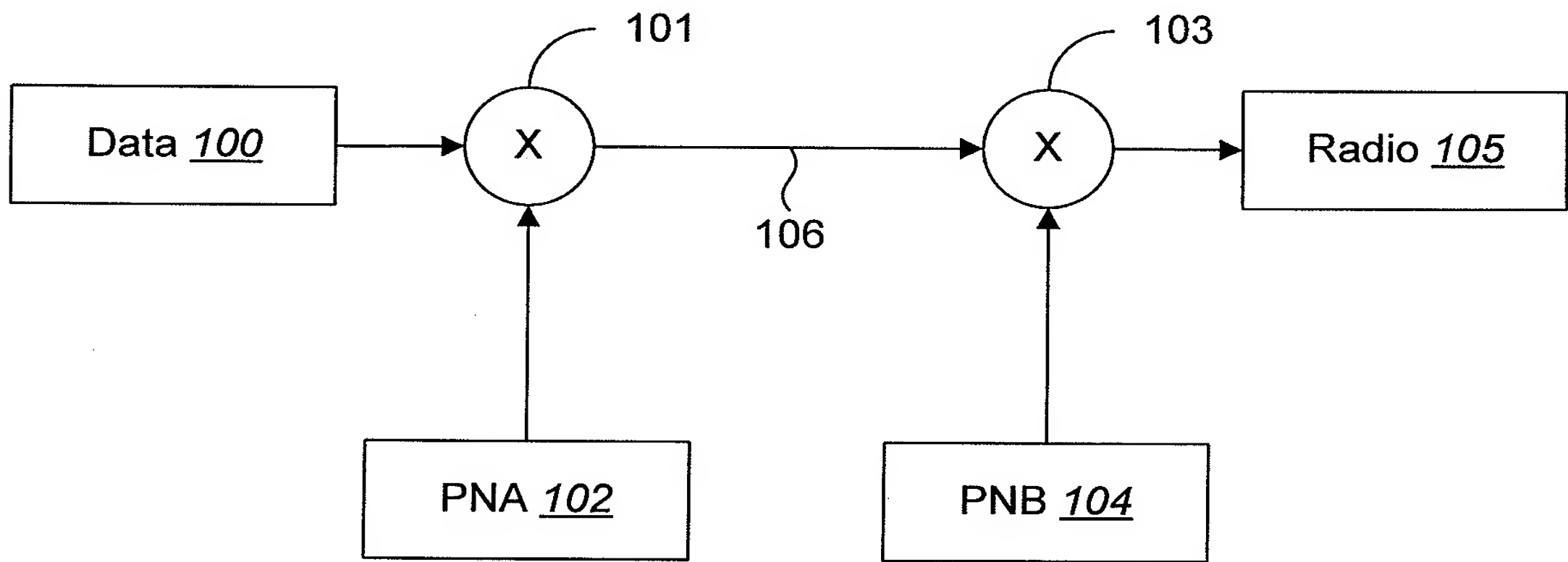


Title: EQUALIZER WITH DECISION FEEDBACK FREQUENCY TRACKING AND BIT
DECODING FOR SPREAD SPECTRUM COMMUNICATIONS

Inventor: W. Kurt Dobson et al.

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Chip/Sec	5000000	5000000	5000000	5000000	5000000	5000000	5000000	5000000	5000000
PNA Length	1	3	7	15	31	63	127	255	511
PNB Length	255	255	255	255	255	255	255	255	255
Chips/Bit	255	765	1785	3825	7905	16065	32385	65025	130305
Processing Gain	24	29	33	36	39	42	45	48	51
DataRate	19608	6536	2801	1307	633	311	154	77	38

FIG. 1

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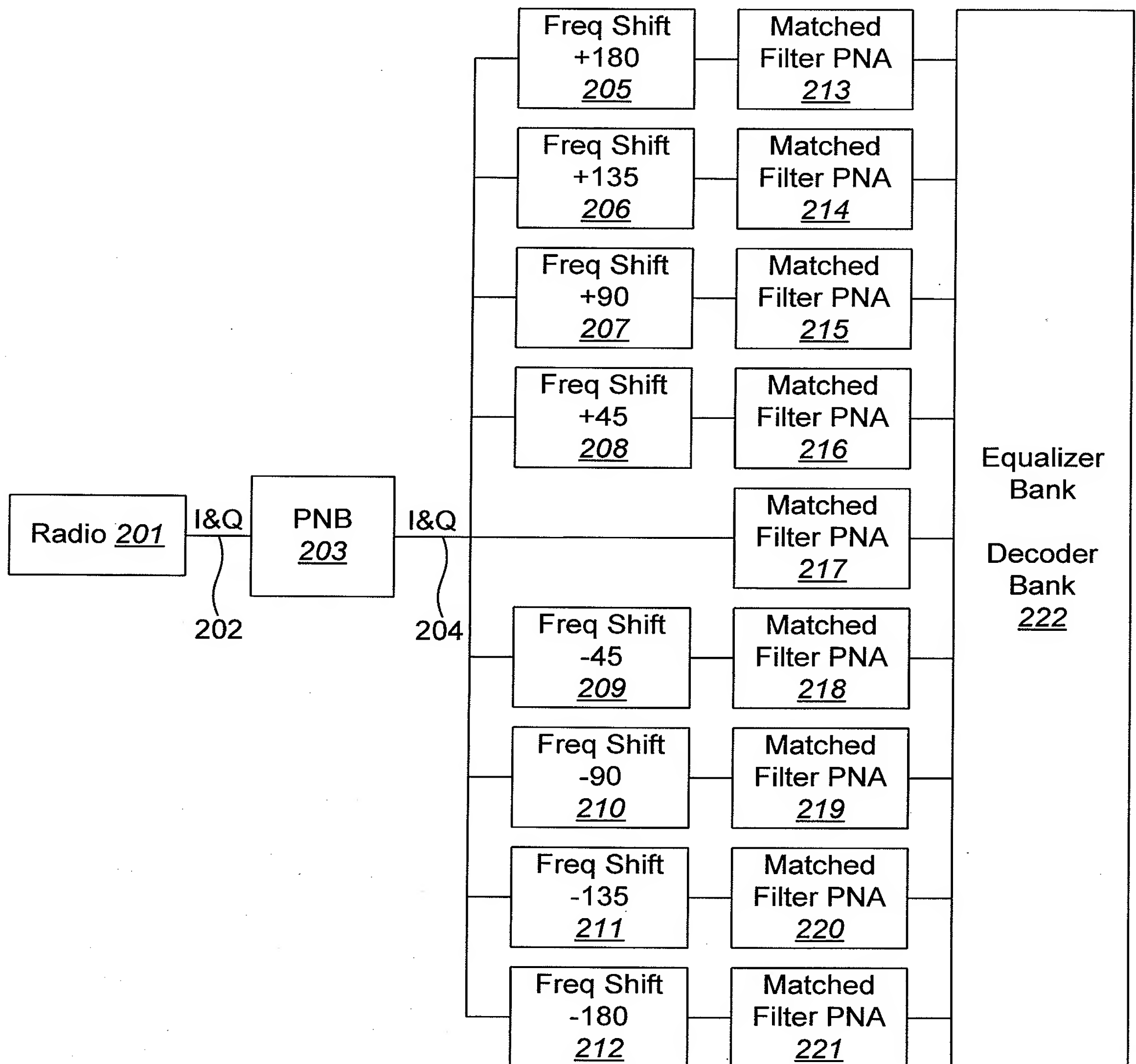


FIG. 2

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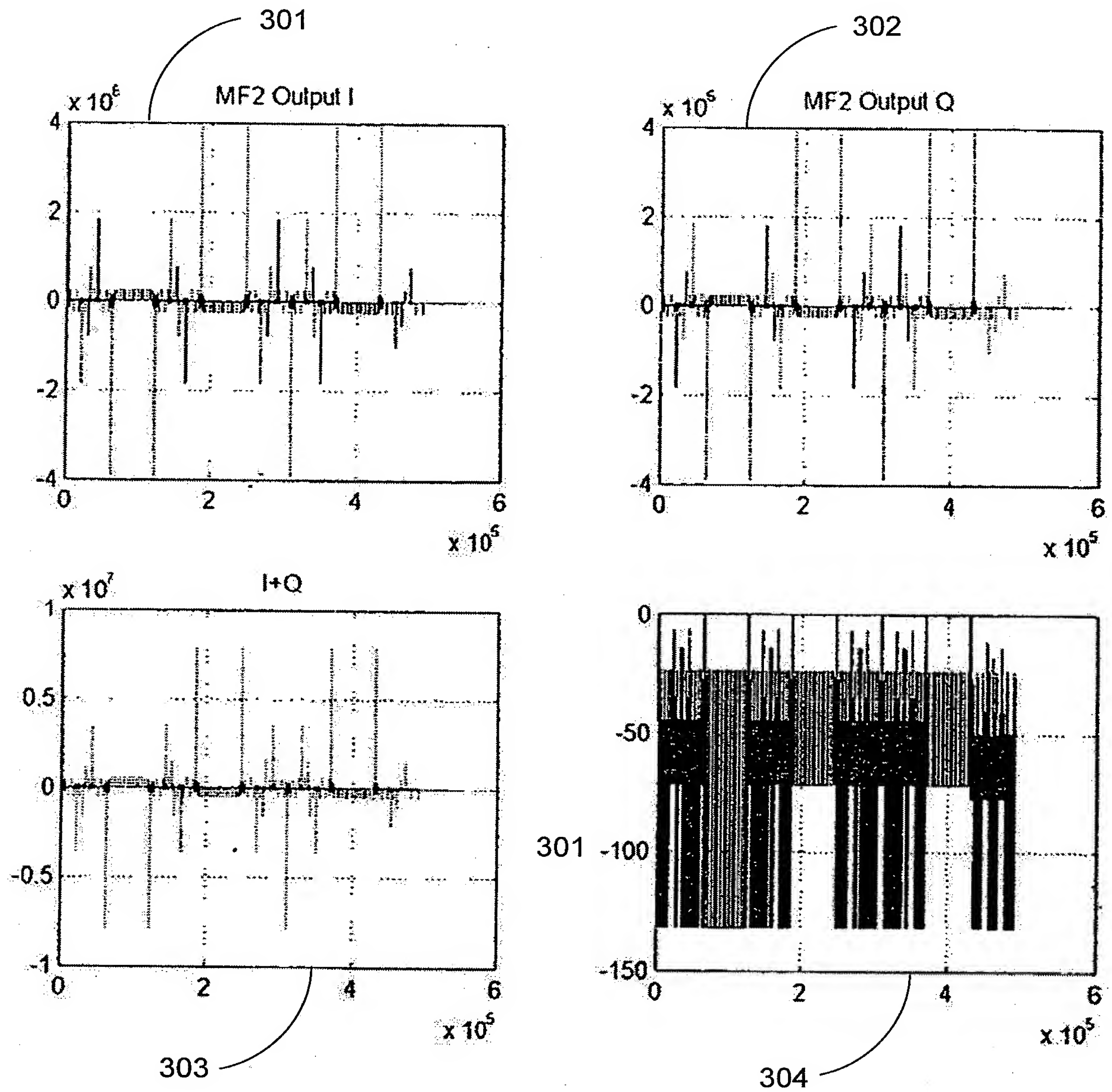


FIG. 3

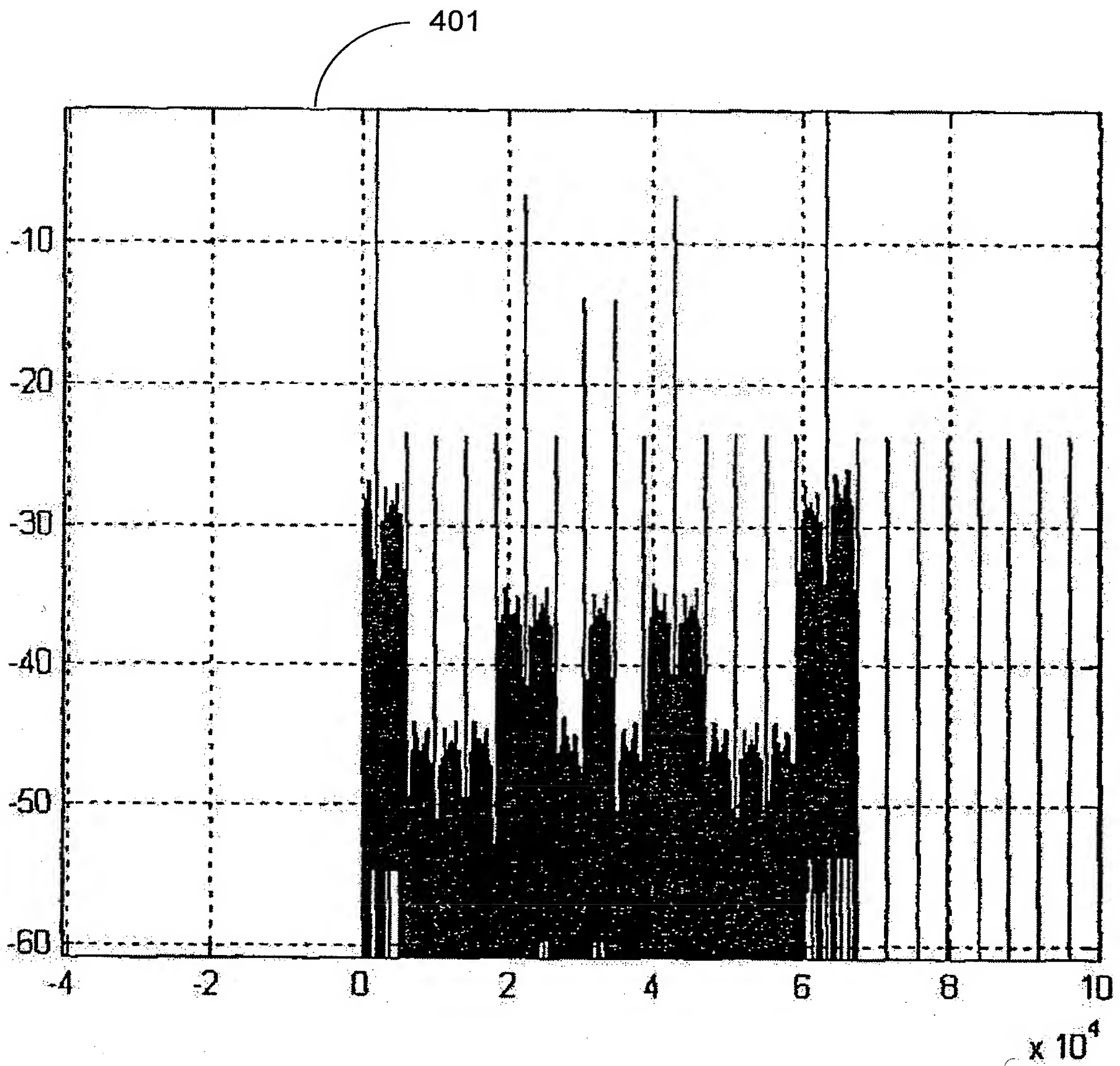


FIG. 4

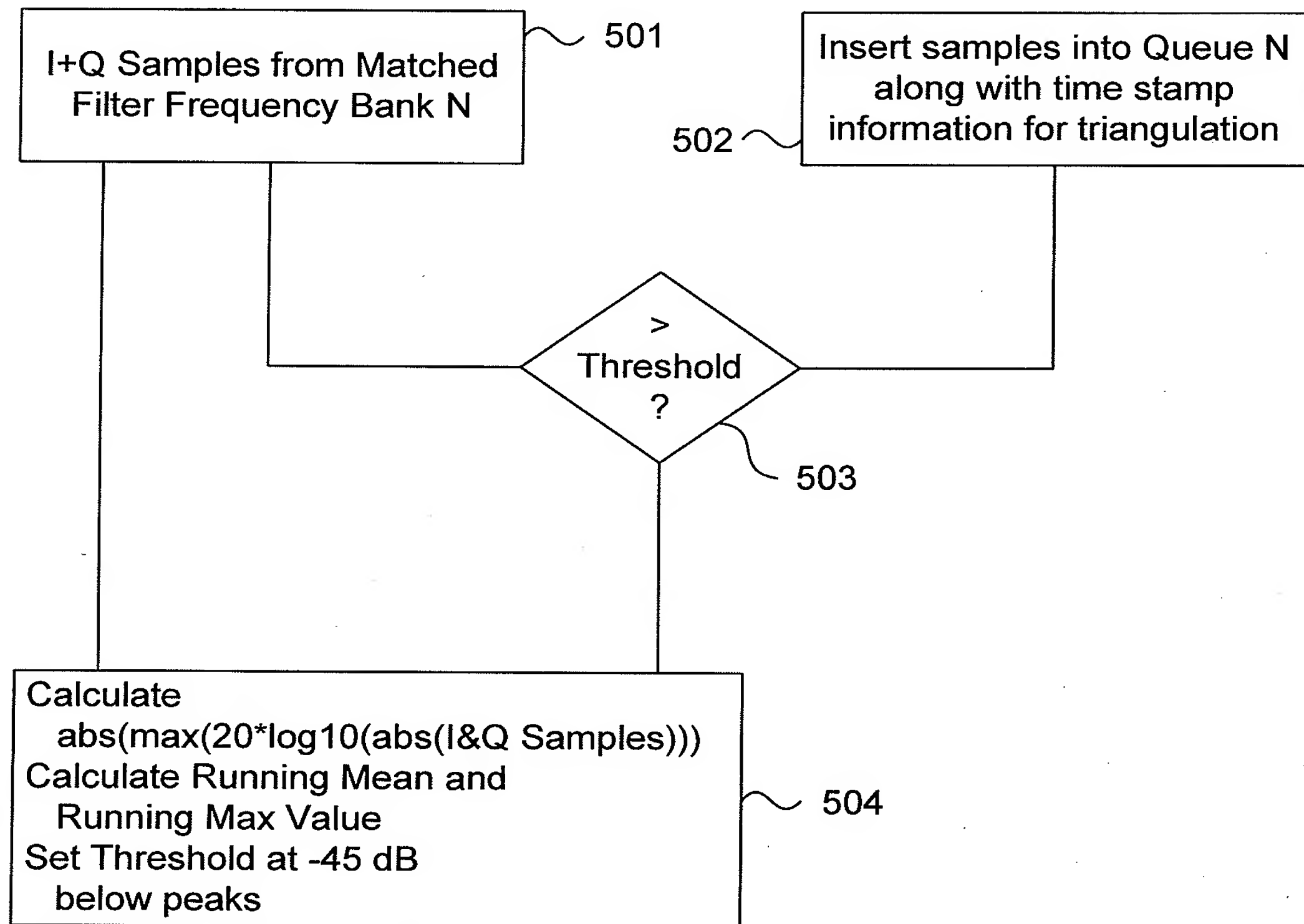


FIG. 5

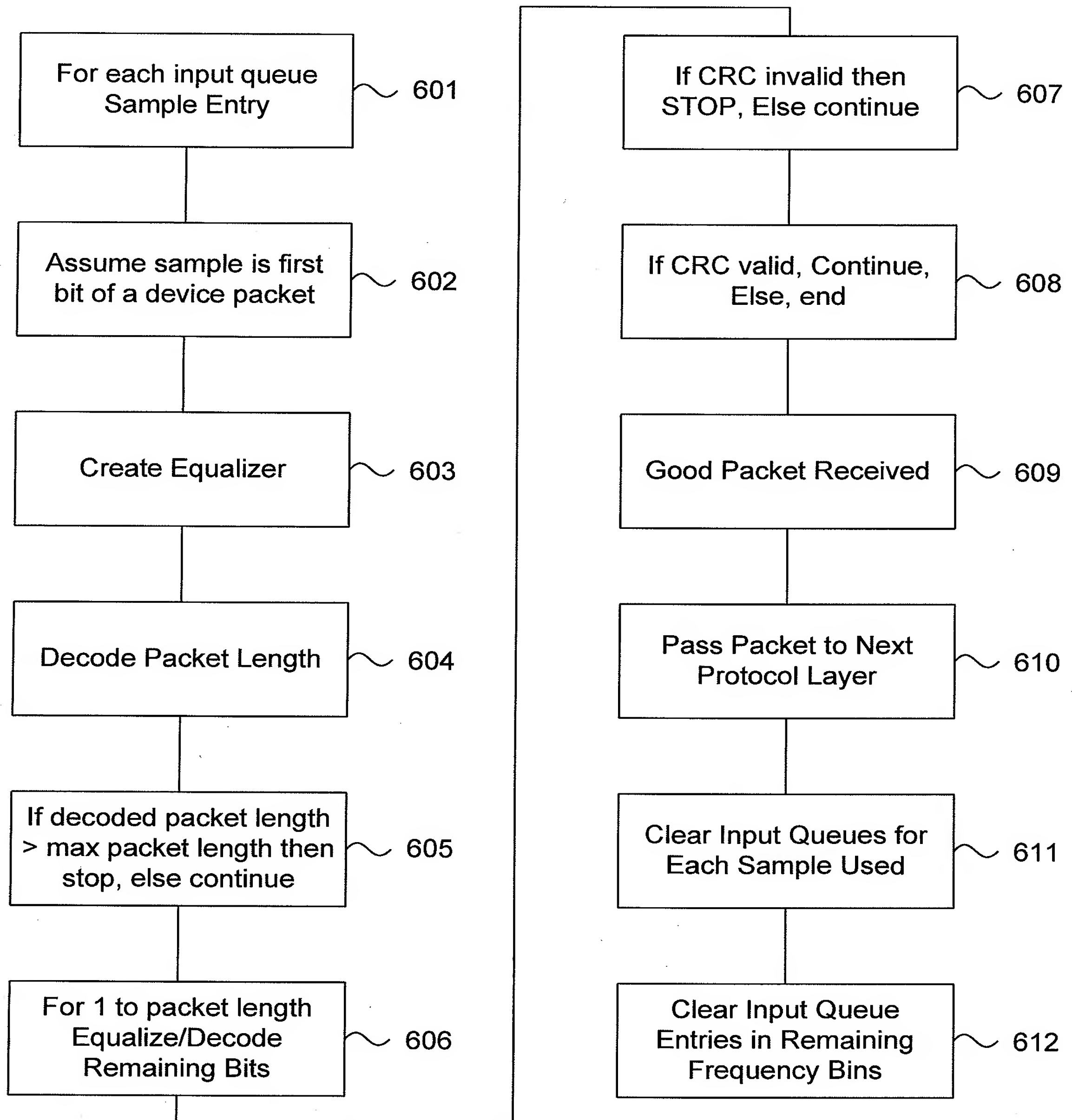


FIG. 6

Title: EQUALIZER WITH DECISION FEEDBACK FREQUENCY TRACKING AND BIT
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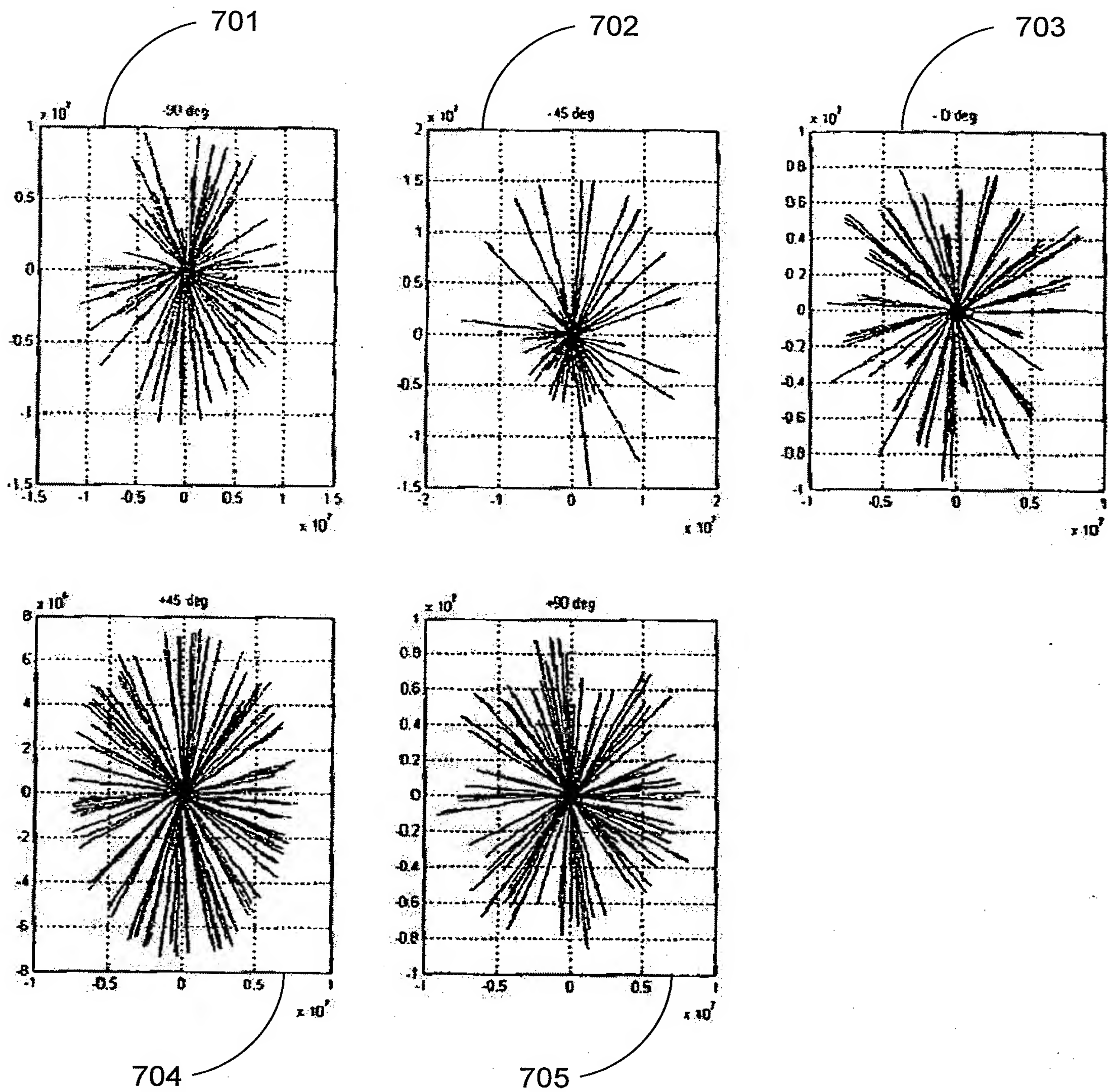


FIG. 7

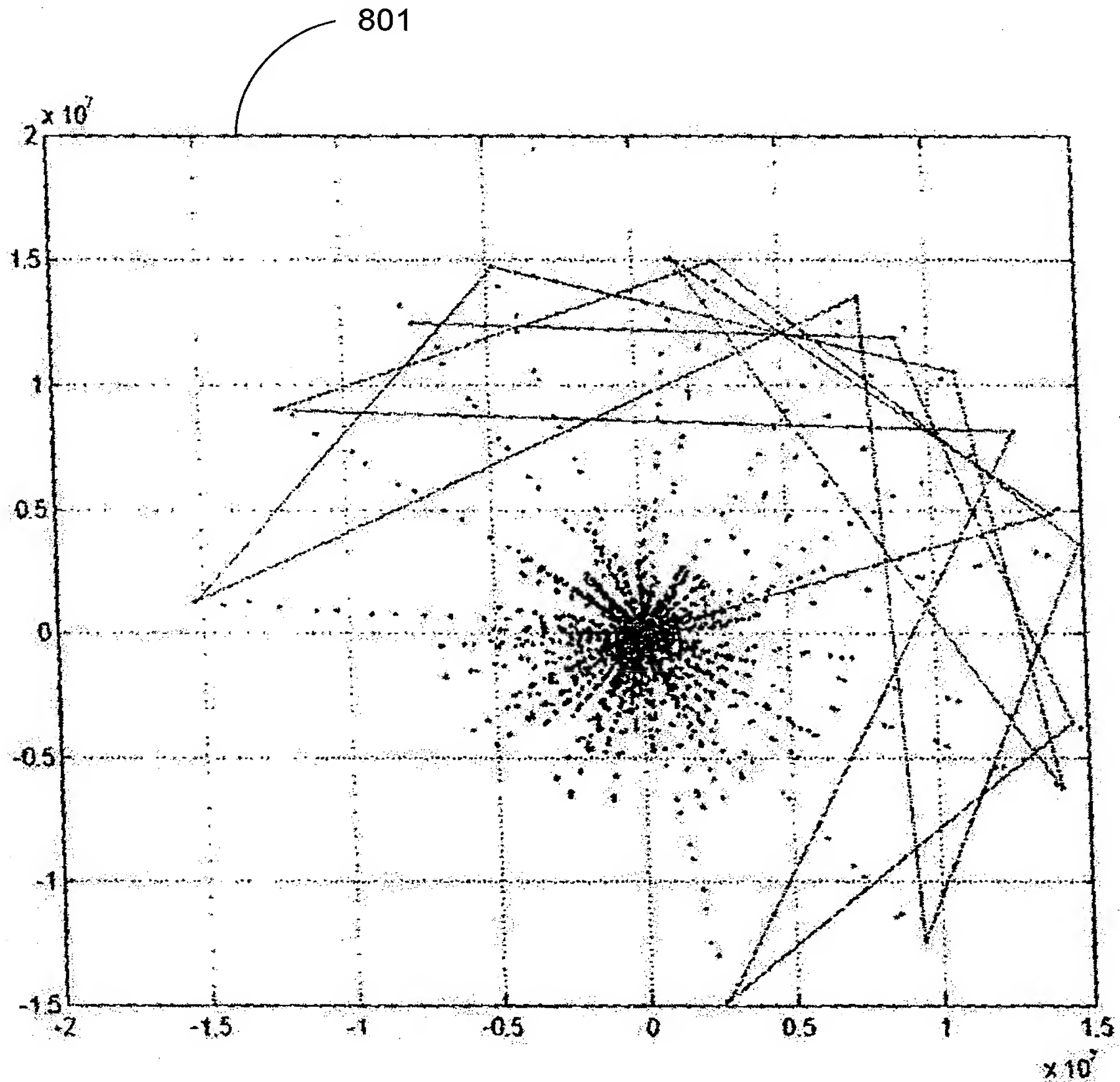


FIG. 8

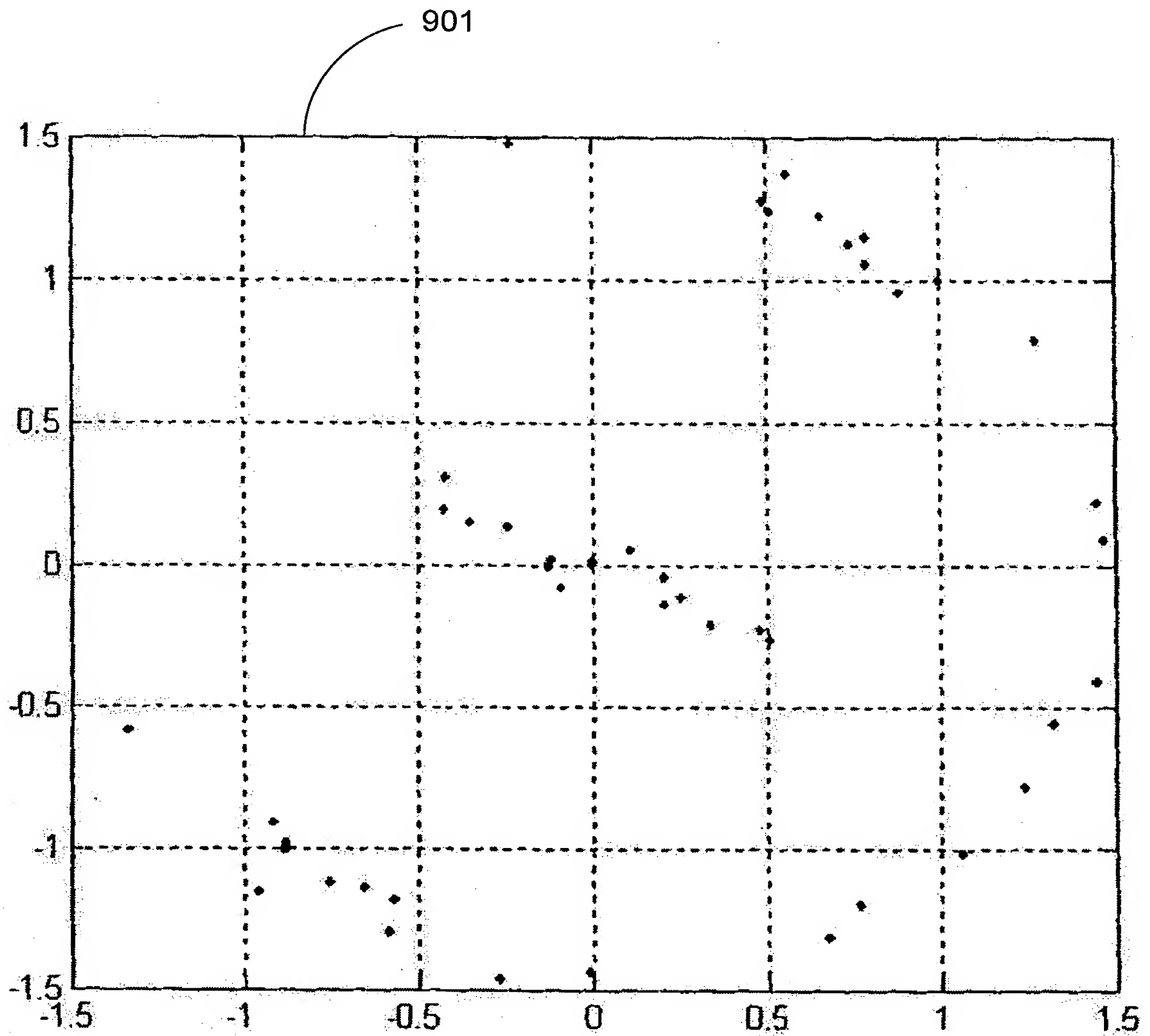


FIG. 9